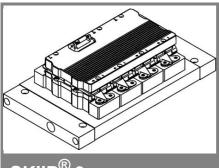
SKiiP 2403GB172-4DW



SKiiP[®] 3

2-pack-integrated intelligent Power System

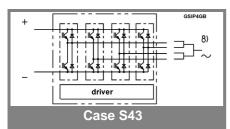
Power section

SKiiP 2403GB172-4DW

Preliminary Data

Power section features

- SKiiP technology inside
- Trench IGBTs
- CAL diode technology
- Integrated current sensor
- Integrated temperature sensor
- Integrated heat sink
- IEC 60721-3-3 (humidity) class 3K3/IE32 (SKiiP[®] 3 System)
- IEC 60068-1 (climate) 40/125/56
- UL recognized file no. E63532
- with assembly of suitable MKP capacitor per terminal
- AC connection busbars must be connected by the user; copper busbars available on request



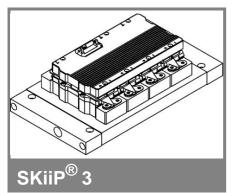
Absolute	Maximum Ratings	$\Gamma_s = 25^{\circ}C$ unless otherwise specified					
Symbol	Conditions	Values	Units				
IGBT							
V _{CES}		1700	V				
V _{CC} ¹⁾	Operating DC link voltage	1200	V				
V _{GES}		±20	V				
Ι _C	T _s = 25 (70) °C	2400 (1800)	А				
Inverse diode							
$I_F = -I_C$	T _s = 25 (70) °C	1800 (1400)	А				
I _{FSM}	T _j = 150 °C, t _p = 10 ms; sin.	13500	А				
I²t (Diode)	Diode, T _j = 150 °C, 10 ms	911	kA²s				
T _j , (T _{stg})		- 40 + 150 (125)	°C				
V _{isol}	rms, AC, 1 min, main terminals to heat sink	4000	V				
I _{AC-terminal}	per AC terminal, rms, T _s = 70 °C,	400	А				
	T _{terminal} <115 °C						

Characte	Characteristics				T _s = 25°	$T_s = 25^{\circ}C$ unless otherwise specified			
Symbol	Conditions			min.	typ.	max.	Units		
IGBT									
V _{CEsat}	I _C = 1200 A measured at te	λ, Τ _j = 25 rminal	(125) °C;			1,9 (2,2)	2,4	V	
V _{CEO}	T _i = 25 (12	5) °C; at t	erminal			1 (0,9)	1,2 (1,1)	V	
r _{CE}	T _i = 25 (125) °C; at terminal				0,8 (1)	1 (1,3)	mΩ		
I _{CES}	$V_{GE} = 0 V, V_{CE} = V_{CES},$ T _i = 25 (125) °C				mA				
$E_{on} + E_{off}$						mJ			
	T _j = 125 °C	, V _{CC} = 1	200 V			1150		mJ	
R _{CC+EE} '	terminal chip, T _i = 25 °C				0,13		mΩ		
L _{CE}	top, bottom					3		nH	
C _{CHC}	per phase,	AC-side				4		nF	
Inverse o	diode								
V _F = V _{EC}	I _F = 1200 A measured at te	, T _j = 25 rminal	(125) °C			2 (1,8)	2,15	V	
V _{TO}	T _i = 25 (12	5) °C				1,1 (0,8)	1,2 (0,9)	v	
r _T	$T_i = 25 (12)$	5) °C				0,8 (0,8)	0,8 (0,9)	mΩ	
E _{rr}	I _C = 1200 A	, V _{CC} = 9	900 V			144		mJ	
	T _j = 125 °C	, V _{CC} = 1	200 V			171		mJ	
Mechani	cal data								
M _{dc}	DC terminals, SI Units				6		8	Nm	
M _{ac}	AC terminals, SI Units			13		15	Nm		
w	SKiiP [®] 3 System w/o heat sink					3,1		kg	
w	heat sink					6,2		kg	
	character								
-	reference	e to bui	lit-in tem	perature	e sensor	(acc. IEC		r i i i i i i i i i i i i i i i i i i i	
R _{th(j-s)I}	per IGBT						0,013	K/W	
R _{th(j-s)D}	per diode					tau	0,025	K/W	
Z _{th}	R _i (mK/W) (max. values)			1					
7	1	2	3	4	1	2	3	4	
Z _{th(j-r)I}	1,2	5 3	5,8 13 5	0 13 5	69 50	0,35 5	0,02	1	
Z _{th(j-r)D}	2	-	13,5	13,5	50		0,25	0,04	
Z _{th(r-a)}	2,7	4,6	1,1	0,6	48	15	2,8	0,4	

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28-02-2007 HER

SKiiP 2403GB172-4DW



2-pack-integrated intelligent Power System

2-pack integrated gate driver SKiiP 2403GB172-4DW

Preliminary Data

Gate driver features

- CMOS compatible inputs
- Wide range power supply
- Integrated circuitry to sense phase current, heat sink temperature and DC-bus voltage (option)
- Short circuit protection
- Over current protection
- Over voltage protection (option)
- Power supply protected against under voltage
- Interlick of top/bottom switch
- Isolation by transformers
- Fibre optic interface (option for GB-types only)
- IEC 60068-1 (climate) 40/85/56
- UL recognized file no. 242581

Absolute	e Maximum Ratings	$T_a = 25^{\circ}$ C unless otherwise specified		
Symbol	Conditions	Values	Units	
V _{S2}	unstabilized 24 V power supply	30	V	
V _i	input signal voltage (high)	15 + 0,3	V	
dv/dt	secondary to primary side	75	kV/µs	
V _{isolIO}	input / output (AC, rms, 2s)	4000	V	
VisoIPD	partial discharge extinction voltage, rms, $Q_{PD} \leq 10 \text{ pC}$;	1500	V	
V _{isol12}	output 1 / output 2 (AC, rms, 2s)	1500	V	
f _{sw}	switching frequency	7	kHz	
f _{out}	output frequency for I _{peak(1)} =I _C	7	kHz	
$T_{op} (T_{stg})$	operating / storage temperature	- 40 + 85	°C	

Characte	eristics	(T _a = 25			= 25°C)
Symbol	Conditions	min. typ. max.			Units
V _{S2}	supply voltage non stabilized	13	24	30	V
I _{S2}	V _{S2} = 24 V	430+45*f/kHz+0,00011*(I _{AC} /A) ²			mA
V _{iT+}	input threshold voltage (High)			12,3	V
V _{iT-}	input threshold voltage (Low)	4,6			V
R _{IN}	input resistance		10		kΩ
C _{IN}	input capacitance		1		nF
t _{d(on)IO}	input-output turn-on propagation time		1,3		μs
t _{d(off)IO}	input-output turn-off propagation time		1,3		μs
t _{pERRRESET}	error memory reset time		9		μs
t _{TD}	top / bottom switch interlock time		3,3		μs
I _{analogOUT}	max. 5mA; 8 V corresponds to 15 V supply voltage for external components		2000		A
I _{s1out}	max. load current			50	mA
I _{TRIPSC}	over current trip level				
	$(I_{analog} OUT = 10 V)$		2500		А
T _{tp}	over temperature protection	110		120	°C
U _{DCTRIP}	U _{DC} -protection (U _{analog OUT} = 9 V);	i	not mplemente	d	V
	(option for GB types)				

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